

MOTOROLA DMR ESSENTIAL PROPERTIES

(as of 30 May 2024)

#1 Title: Method For Selecting An Operating Mode Based On A Detected Synchronization Pattern				
Inventor(s): Hiben, Bradley M; Biggs, Robert A; Muri, David L; Newberg, Donald G; Stogner, Darrell J; Wilson, Alan L				
Country	App. #	App. Date	Patent #	Grant Date
AU	2004246135	29-Apr-2004	2004246135	4-Jan-2008
CA	2527142	29-Apr-2004	2527142	7-Apr-2009
GB	0523928.0	29-Apr-2004	2418107	4-Jun-2008
HK	06103991.7	29-Apr-2004	HK1082608B	21-Dec-2007
MY	PI20041885	19-May-2004	MY-134526-A	30-Nov-2007
US	10/799828	12-Mar-2004	7203207	10-Apr-2007
US	11/668878	30-Jan-2007	8306071	6-Nov-2012
#2 Title: Method For Selectively Allocating A Limited Number Of Bits To Support Multiple Signaling Types On A Low Bit Rate Channel				
Inventor(s): Newberg, Donald G; Biggs, Robert A; Hiben, Bradley M; Muri, David L; Stogner, Darrell J; Wilson, Alan L				
Country	App. #	App. Date	Patent #	Grant Date
AU	2004246136	29-Apr-2004	2004246136	5-Apr-2007
CA	2526926	29-Apr-2004	2526926	1-Dec-2009
GB	0523205.3	29-Apr-2004	2418812	28-Feb-2007
MY	PI20041886	19-May-2004	MY-134217-A	30-Nov-2007
US	10/799913	12-Mar-2004	7339917	4-Mar-2008

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#3 Title: Method Of Signaling Reverse Channel Information With Minimal Voice/Data Delay				
Inventor(s): Biggs, Robert A				
Country	App. #	App. Date	Patent #	Grant Date
AU	2005226531	22-Feb-2005	2005226531	5-Jun-2008
BR	PI0508671-0	22-Feb-2005	1911	26-Mar-2019
CA	2558551	22-Feb-2005	2558551	26-Oct-2010
CN	200580007661.6	22-Feb-2005	ZL200580007661.6	22-Jun-2011
GB	0619700.8	22-Feb-2005	2429378	8-Apr-2009
JP	2007-500911	22-Feb-2005	4351720	31-Jul-2009
KR	10-2006-7018691	22-Feb-2005	848483	21-Jul-2008
MX	PA/a/2006/010307	22-Feb-2005	260940	1-Oct-2008
NZ	549124	22-Feb-2005	549124	10-Jan-2008
RU	2006136026	22-Feb-2005	2321952	10-Apr-2008
US	10/799035	12-Mar-2004	7200129	3-Apr-2007
#4 Title: Method For Allowing A Subscriber Unit To Access A Channel For Voice Transmissions				
Inventor(s): Wiatrowski, David G; Bohn, Thomas B; Senese, Thomas J; Mansfield, Terry K; Belmonte, John P				
Country	App. #	App. Date	Patent #	Grant Date
AU	2005262626	6-Jun-2005	2005262626	18-Sep-2008
AU	2008211998	6-Jun-2005	2008211998	25-Feb-2010
BR	PI0512052-7	6-Jun-2005	1935	17-Jul-2019
CA	2570441	6-Jun-2005	2570441	7-Feb-2012
CN	200580021766.7	6-Jun-2005	ZL200580021766.7	9-May-2012
GB	0624706.8	6-Jun-2005	2430333	7-Jan-2009
JP	2007-515678	6-Jun-2005	4690397	25-Feb-2011
KR	10-2006-7027865	6-Jun-2005	10-869043	11-Nov-2008
MX	pA/a/2006/015241	6-Jun-2005	264609	13-Feb-2009
NZ	551231	6-Jun-2005	551231	13-Aug-2009
RU	2006147260	6-Jun-2005	2351080	27-Mar-2009
US	10/984431	9-Nov-2004	7499441	3-Mar-2009

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#5 Title: Method And System of Interrupting A Transmitting Subscriber In A Wireless Communications System *				
Inventor(s): Wiatrowski, David G; Belmonte, John P; Chowdhary, Dipendra M				
Country	App. #	App. Date	Patent #	Grant Date
AU	2007223360	2-Mar-2007	2007223360	6-Jan-2011
CN	200780007705.4	2-Mar-2007	ZL200780007705.4	18-Sep-2013
GB	0817895.6	2-Mar-2007	GB2451015	29-Jun-2011
RU	2008139316	2-Mar-2007	2418392	10-May-2011
* This patent family has been declared to ETSI on a royalty-free basis.				
#6 Title: Method And Apparatus For Error Detection In A Data Block				
Inventor(s): Wiatrowski, David G; Bohn, Thomas B; Doberstein, Kevin G; Newberg, Donald G				
Country	App. #	App. Date	Patent #	Grant Date
AU	2007284221	27-Jul-2007	2007284221	25-Mar-2010
BR	PI0708491-9	27-Jul-2007	0708491-9	19-Nov-2019
CA	2642102	27-Jul-2007	2642102	20-Dec-2011
CN	200780010467.2	27-Jul-2007	ZL200780010467.2	14-Aug-2013
DE	07799864.9	27-Jul-2007	602007052293.5	6-Sep-2017
ES	07799864.9	27-Jul-2007	EP2052482	6-Sep-2017
FR	07799864.9	27-Jul-2007	EP2052482	6-Sep-2017
GB	07799864.9	27-Jul-2007	EP2052482	6-Sep-2017
IT	07799864.9	27-Jul-2007	502017000139261	6-Sep-2017
JP	2008-556589	27-Jul-2007	5282574	7-Jun-2013
MX	MX/a/2008/010665	27-Jul-2007	284079	16-Feb-2011
NL	07799864.9	27-Jul-2007	EP2052482	6-Sep-2017
NZ	570506	27-Jul-2007	570506	8-Jan-2011
PL	07799864.9	27-Jul-2007	EP2052482	6-Sep-2017
RU	2008145754	27-Jul-2007	2392750	20-Jun-2010
US	11464369	14-Aug-2006	7500170	3-Mar-2009

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#7 Title: Method For Synchronizing Direct Mode Time Division Multiple Access (TDMA) Transmissions				
Inventor(s): Chowdhary, Dipendra M; Wiatrowski, David G				
Country	App. #	App. Date	Patent #	Grant Date
BR	BR112012026181-9	27-Dec-2010	1120120261819	23-Mar-2021
CN	201080066204.5	27-Dec-2010	ZL201080066204.5	24-Jun-2015
DE	10800864.0	27-Dec-2010	602010031479.0	23-Mar-2016
FR	10800864.0	27-Dec-2010	EP2559300	23-Mar-2016
GB	10800864.0	27-Dec-2010	EP2559300	23-Mar-2016
MX	MX/a/2012/011980	27-Dec-2010	312894	2-Sep-2013
RU	2012148548	27-Dec-2010	2533664	20-Nov-2014
US	13/557903	25-Jul-2012	8457104	4-Jun-2013
US	12/760787	15-Apr-2010	8599826	3-Dec-2013
#8 Title: Method For Direct Mode Channel Access				
Inventor(s): Bohn, Thomas B; Wiatrowski, David G				
Country	App. #	App. Date	Patent #	Grant Date
AU	2011240989	24-Mar-2011	2011240989	20-Mar-2014
BR	BR112012026189-4	24-Mar-2011	1120120261894	21-Sep-2021
CA	2796286	24-Mar-2011	2796286	17-May-2016
DE	11769263.2	24-Mar-2011	602011043803.4	29-Nov-2017
FR	11769263.2	24-Mar-2011	EP2559170	29-Nov-2017
GB	11769263.2	24-Mar-2011	EP2559170	29-Nov-2017
IT	11769263.2	24-Mar-2011	EP2559170	29-Nov-2017
US	12/761023	15-Apr-2010	8503409	6-Aug-2013